

Fig. 1. Battery Node Block Diagram.

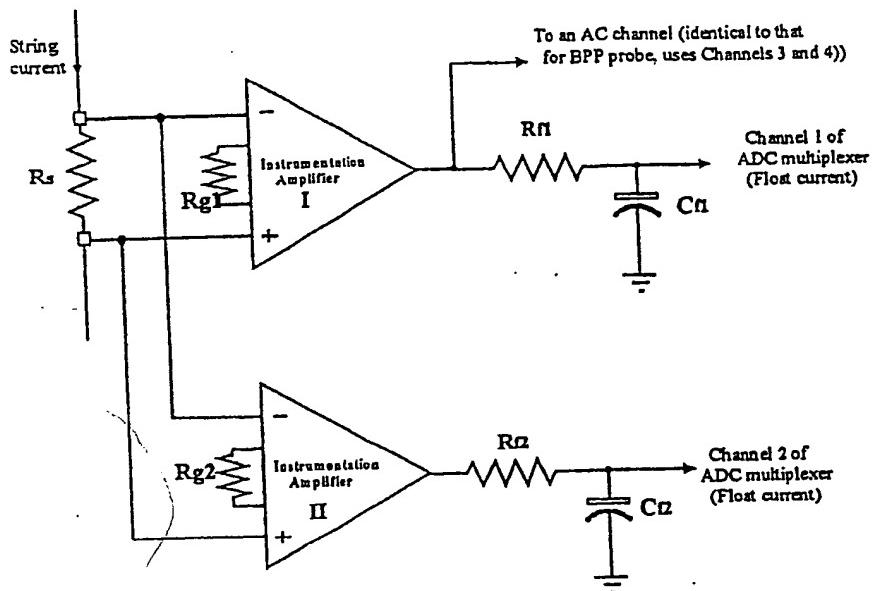


Fig.2. Current Probe Analog Front End

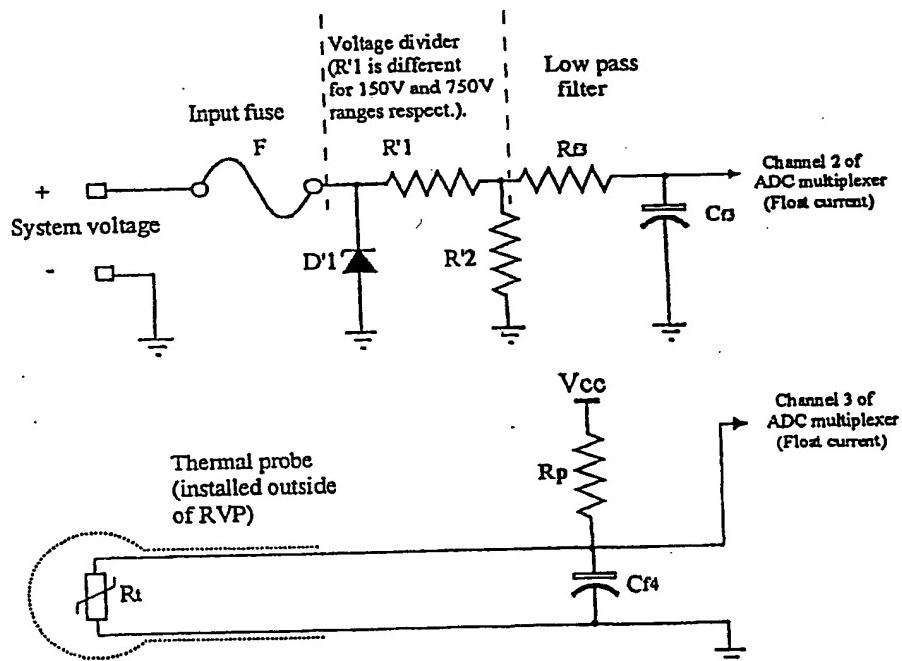


Fig. 3. Rectifier Voltage Probe Analog Front End

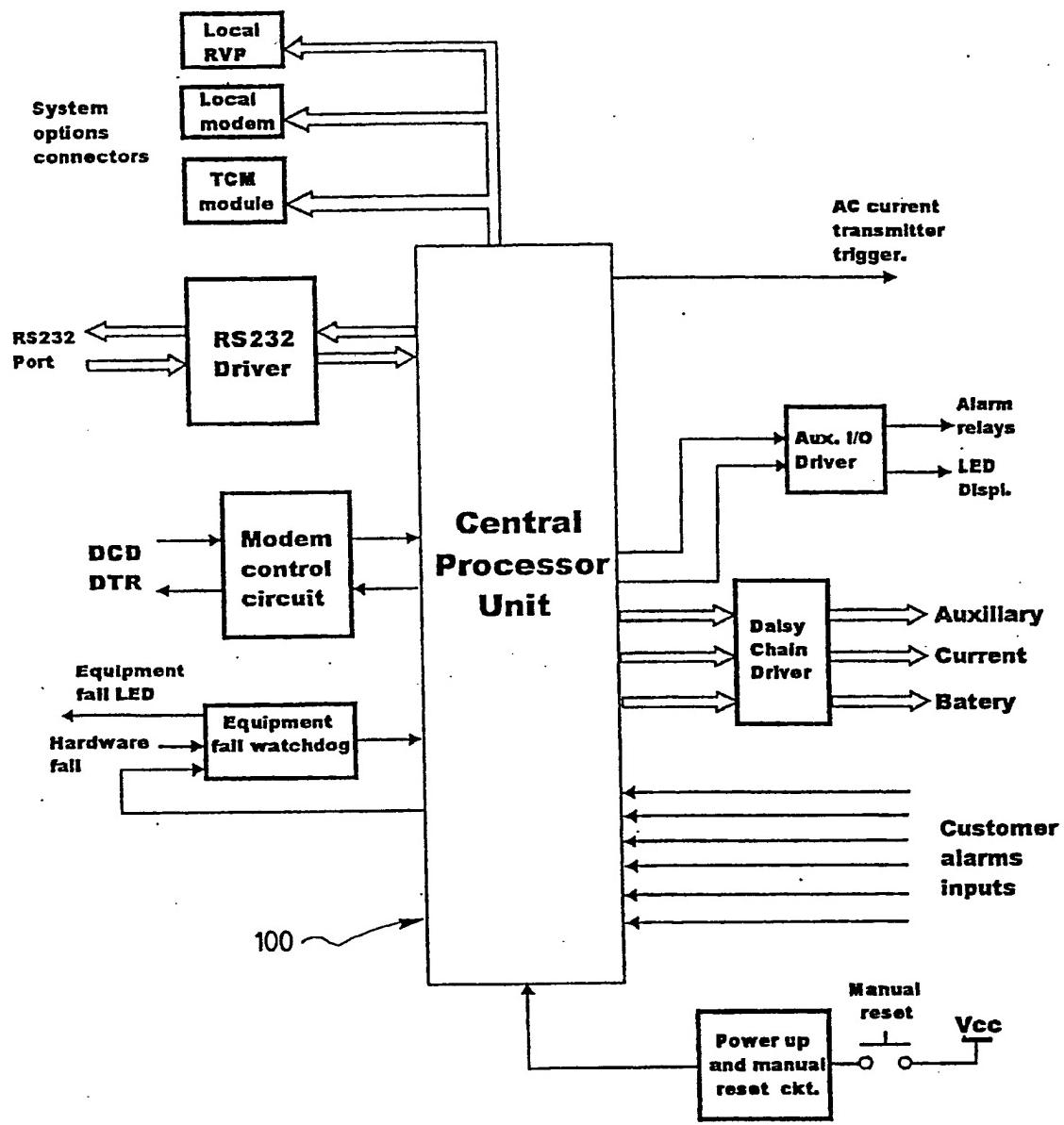


Fig. 4. System server block diagram

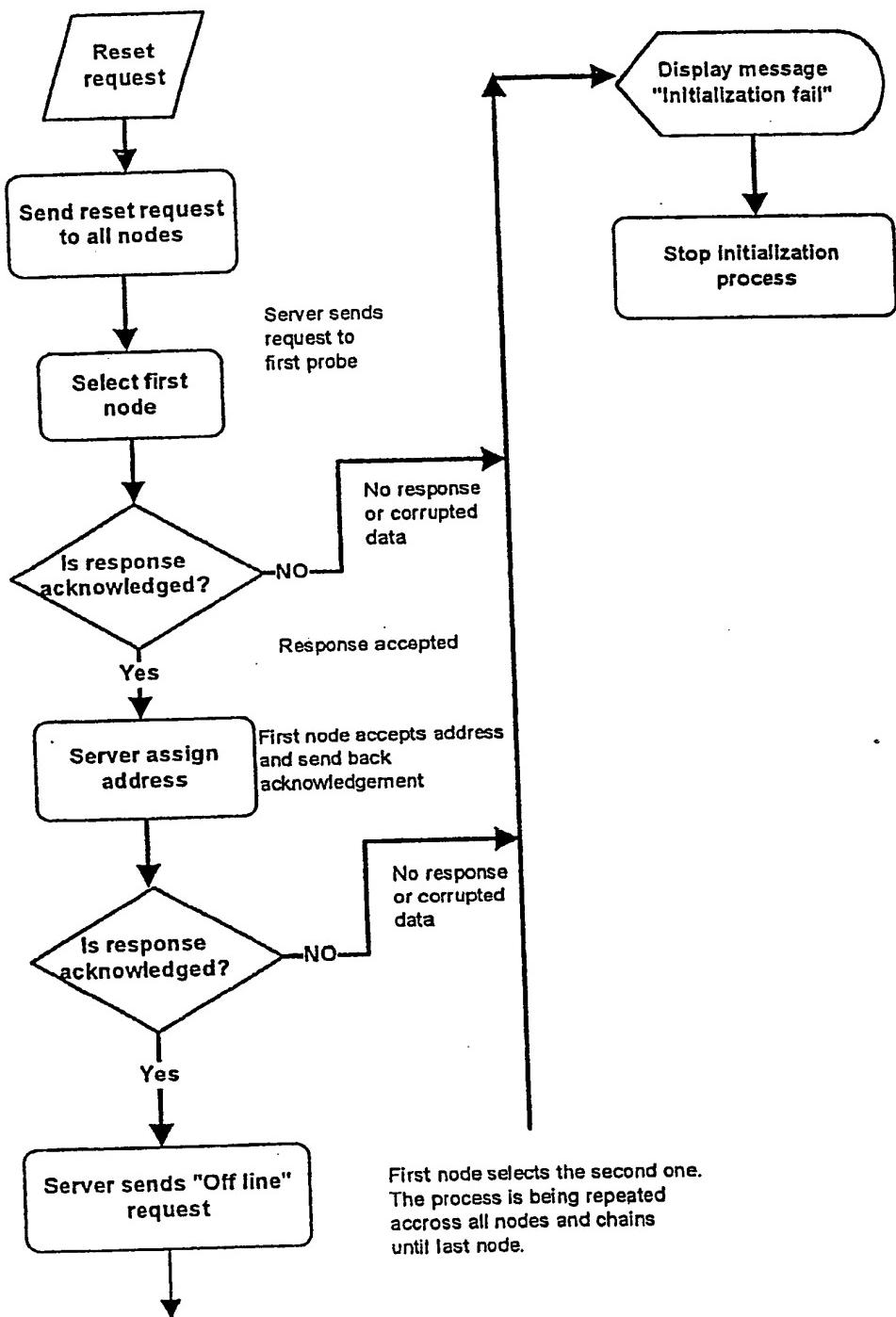


Fig. 5. Daisy chain reset and addressing process

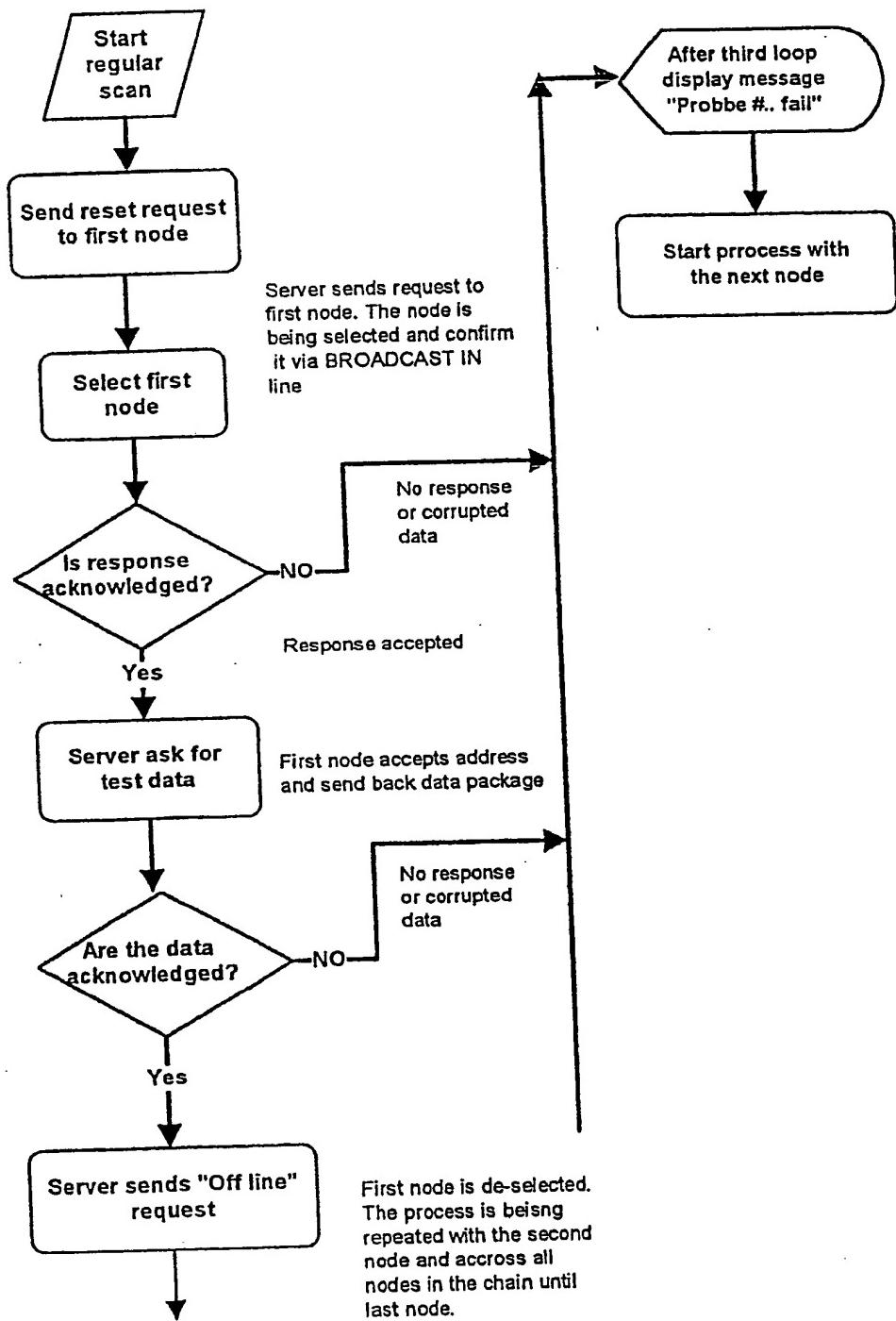


Fig. 6. Daisy chain regular scan

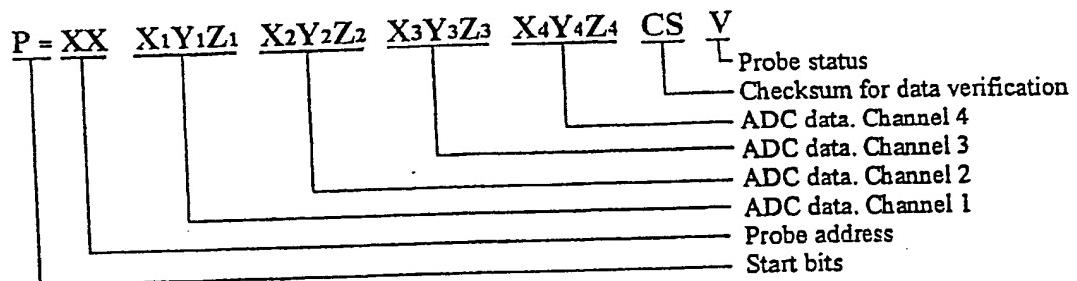


Fig. 7. Daisy chain communication data structure

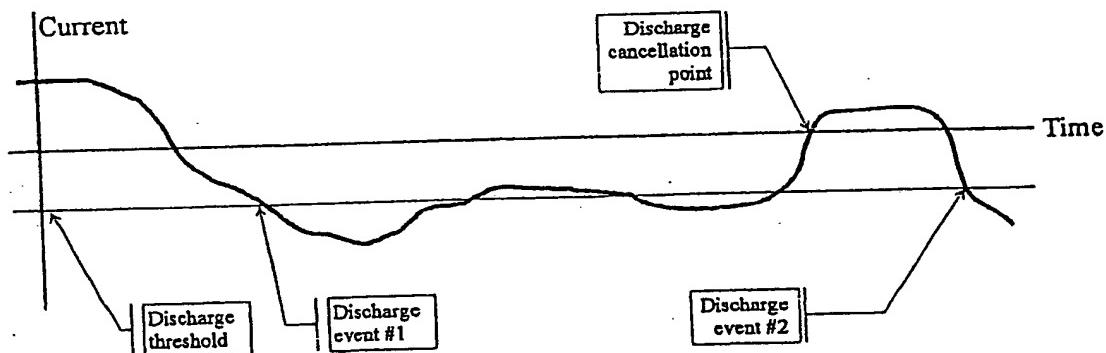


Fig. 8. Discharge event definition

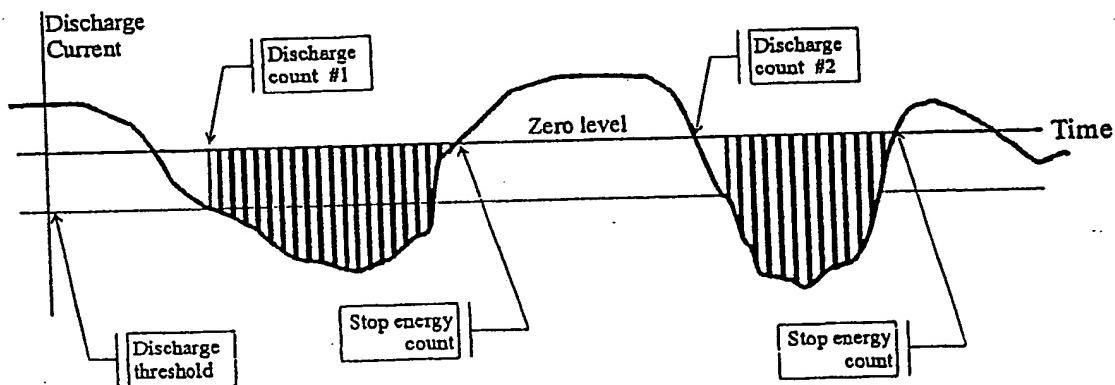


Fig. 9. Graphical presentation of discharge events and energy count

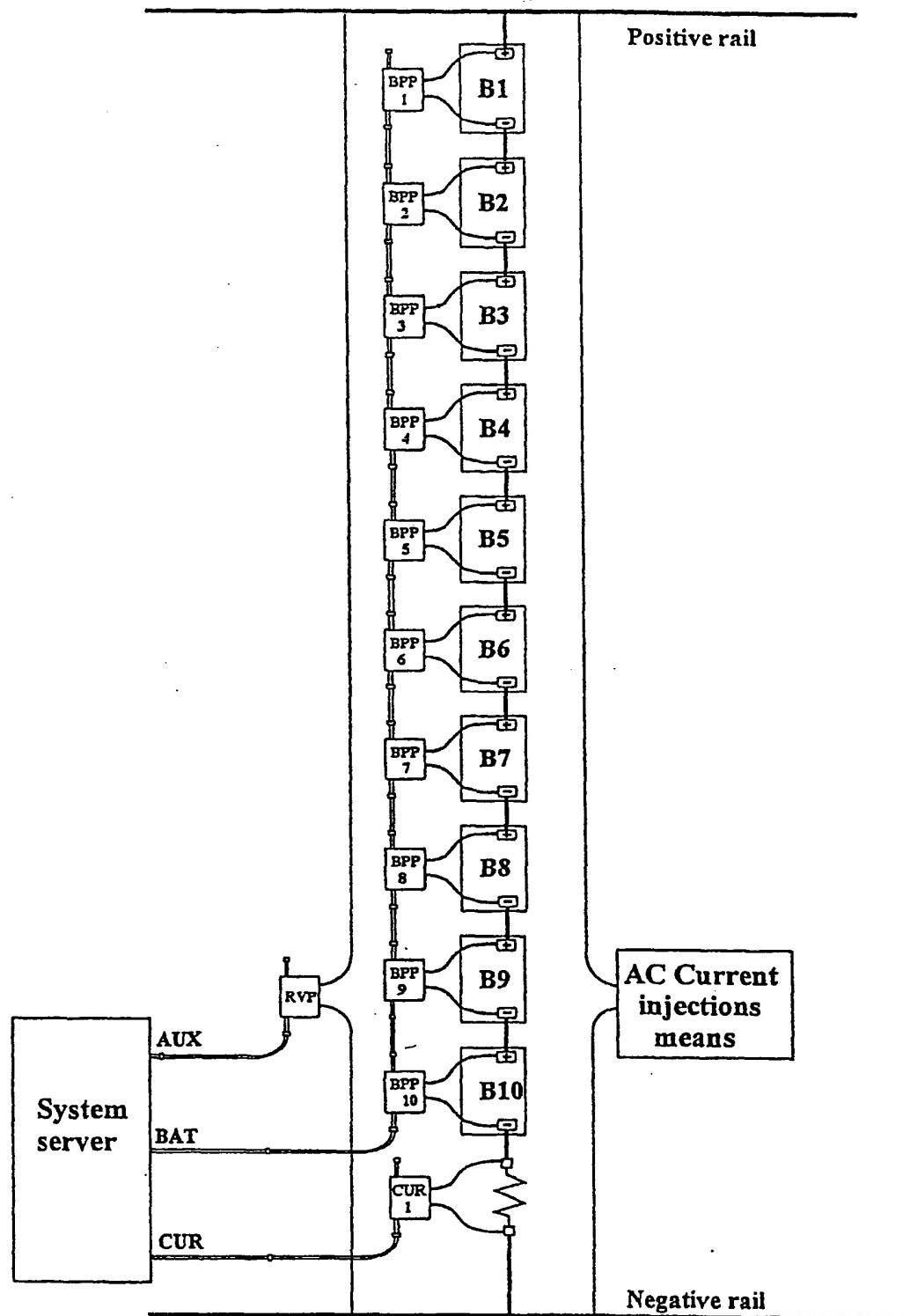


Fig. 10. Monitoring system block diagram